

Notice of References Cited	Application/Control No. 10/691,966	Applicant(s)/Patent Under Reexamination ABDEL-HAFEZ ET AL.	
	Examiner John P. Trimmings	Art Unit 2133	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-2002/0120896	08-2002	Wang et al.	714/731
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	M	US-			

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NON-PATENT DOCUMENTS

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	U	Lattice Semiconductor, "ORCA(r) Series 4 FPGAs", January, 2003, Data Sheet by Lattice Semiconductor, see internet address attached, accessed 9/1/2005.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.